

Abstract

Circuit arrangement for detecting the state of at least one electrical switch having at least one set input (Set1, Set2, Set3) in each case and at least one
5 sensor output (Sensor1, Sensor2, Sensor3) in each case, each of which is allocated to an electrical switch (S1, S2, S3), wherein the sensor output (Sensor1, Sensor2, Sensor3) is connected via a first current path to a first potential if a signal with the value logic "1" is present at the set input (Set1, Set2, Set3) and is connected via a
10 second current path to a second potential if a signal with the value logic "0" is present at the set input (Set1, Set2, Set3), and wherein a wake-up signal is generated at a wake-up signal output (wakeup) if a current which is greater than a predetermined threshold current is detected in the first or second current path.